

## **Silicon Creations' PLL Technology Contributes to the First 7nm Mass Production Mining Chip**

Lawrenceville, Ga. – February 12, 2019 – [Silicon Creations](#), a leading supplier of high-performance analog and mixed-signal intellectual property (IP), announced that its phase-locked loop (PLL) technology has been deployed in Canaan Creative's 7nm ASIC chip.

[Canaan](#), the leader in offering blockchain hardware and software solutions and designing ASIC chips [announced](#) in August 2018 that they had started selling their new Avalonminer product based on their 7nm chips which are mass produced at TSMC. Canaan's chips are known as the global leader in blockchain repetitive computing.

“We congratulate Canaan on this ground-breaking chip and are proud to be part of Canaan's success,” said Andrew Cole, vice president at Silicon Creations. “We've now provided PLLs for both their 7nm and 16nm Miner chips, and look forward to continuing to provide PLL technology to Canaan to help them maintain their technology leadership position as their products evolve.”

Silicon Creations produces a wide range of PLLs for most advanced process nodes. These include general-purpose Fractional-N PLLs, IoT PLLs with 32kHz RTC reference clock, Low-area Core voltage PLLs and Deskew PLLs for DDR interfaces. The company's PLLs are used in almost every vertical market and are an excellent choice for smart phones, battery-operated products, networking chips, energy-efficient and IoT chips, portable audio, set-top boxes, flat panel displays, high-performance computing, and mass storage.

“Silicon Creations provided high quality verified PLL IP on the dates promised and with excellent customer support,” said Mark Wu, VP of Technology at Canaan Creative. “This collaboration allowed us to maintain a tight production schedule and meet our target specifications on performance and power. We're delighted the PLL worked as promised with first silicon”

## **About Silicon Creations**

Silicon Creations is focused on providing world-class silicon intellectual property (IP) for precision and general-purpose timing PLLs, SerDes and high-speed differential I/Os. Silicon Creations' IP is proven from 7- to 180-nanometer process technologies. With a complete commitment to customer success, its IP has an excellent record of first silicon to mass production in customer designs. Silicon Creations, founded in 2006, is self-funded and growing. The company has development centers in Atlanta, Ga., and Krakow, Poland, and worldwide sales representation. For more information, visit [www.siliconcr.com](http://www.siliconcr.com).

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